

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of: )  
 )  
**Shigehiko SAIDA et al.** ) **Group Art Unit: Not Yet Assigned**  
 )  
**Serial No.: Not Yet Assigned** ) **Examiner: Not Yet Assigned**  
 )  
**Filed: February 4, 2004** )  
 )  
**For: A SEMICONDUCTOR DEVICE HAVING** )  
**TRENCH CAPACITORS AND METHOD FOR** )  
**MAKING THE TRENCH CAPACITORS** )

**MAIL STOP PATENT APPLICATION**  
**Commissioner for Patents**  
**P.O. Box 1450**  
**Alexandria, VA 22313-1450**

Sir:

**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§1.56 and 1.97(b), applicants bring to the Examiner's attention the document listed on attached Form PTO-1449. A copy of the listed document is attached. Applicants respectfully request that the Examiner consider the document listed on attached Form PTO-1449 and indicate that it was considered by making an appropriate notation on this form. This Information Disclosure Statement is being filed with the above-referenced application.

The following is a concise statement of relevance of the non-English language documents:

1. Japanese Patent Publication No. 2002-016236 - discloses manufacturing of semiconductor devices include a capacitance increase method.

Also, an English-language abstract of this document is enclosed

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This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitutes "prior art." If the Examiner applies the document as prior art against any claim in the application and applicants determine that the cited document does not constitute "prior art" under United States law, applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of such document. Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should it be applied against the claims of the present application.

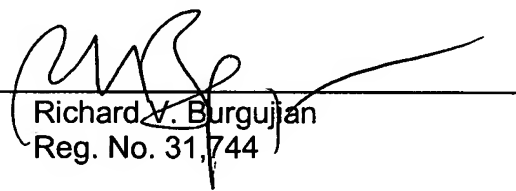
If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

Dated: February 4, 2004

By:

  
Richard V. Burgujian  
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Enclosures  
RVB/FPD/sci

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## INFORMATION DISCLOSURE CITATION

Atty. Docket No.	03180.0352	Serial No.	Not Yet Assigned
Applicants	Shigehiko SAIDA et al.		
Filing Date	February 4, 2004	Group:	Not Yet Assigned

U.S. PATENT DOCUMENTS							
Examiner Initial*		Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS							
		Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
		2002-016236	01/18/02	JAPAN			ABSTRACT

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	

Examiner	Date Considered
*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	
Form PTO 1449	Patent and Trademark Office - U.S. Department of Commerce